

## DM7403 Quad 2-Input NAND Gates with Open-Collector Outputs

### General Description

This device contains four independent gates each of which performs the logic NAND function. The open-collector outputs require external pull-up resistors for proper logical operation.

Where:  $N_1 (I_{OH})$  = total maximum output high current for all outputs tied to pull-up resistor

$N_2 (I_{IH})$  = total maximum input high current for all inputs tied to pull-up resistor

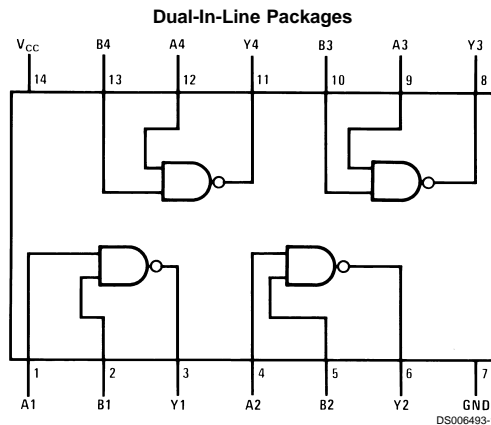
$N_3 (I_{IL})$  = total maximum input low current for all inputs tied to pull-up resistor

### Pull-Up Resistor Equations

$$R_{MAX} = \frac{V_{CC} (Min) - V_{OH}}{N_1 (I_{OH}) + N_2 (I_{IH})}$$

$$R_{MIN} = \frac{V_{CC} (Max) - V_{OL}}{I_{OL} - N_3 (I_{IL})}$$

### Connection Diagram



Order Number DM5403J or DM7403N  
See Package Number J14A or N14A

### Function Table

$$Y = \overline{AB}$$

Inputs		Output
A	B	Y
L	L	H
L	H	H
H	L	H
H	H	L

H = High Logic Level  
L = Low Logic Level

**Absolute Maximum Ratings** (Note 1)

Supply Voltage	7V
Input Voltage	5.5V
Output Voltage	7V

## Operating Free Air Temperature Range

DM54	-55°C to +125°C
DM74	0°C to +70°C
Storage Temperature Range	-65°C to +150°C

**Recommended Operating Conditions**

Symbol	Parameter	DM5403			DM7403			Units
		Min	Nom	Max	Min	Nom	Max	
V <sub>CC</sub>	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
V <sub>IH</sub>	High Level Input Voltage	2			2			V
V <sub>IL</sub>	Low Level Input Voltage			0.8			0.8	V
V <sub>OH</sub>	High Level Output Voltage			5.5			5.5	V
I <sub>OL</sub>	Low Level Output Current			16			16	mA
T <sub>A</sub>	Free Air Operating Temperature	-55		125	0		70	°C

**Note 1:** The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

**Electrical Characteristics**

over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 2)	Max	Units
V <sub>I</sub>	Input Clamp Voltage	V <sub>CC</sub> = Min, I <sub>I</sub> = -12 mA			-1.5	V
I <sub>CEX</sub>	High Level Output Current	V <sub>CC</sub> = Min, V <sub>O</sub> = 5.5V V <sub>IL</sub> = Max			250	μA
V <sub>OL</sub>	Low Level Output Voltage	V <sub>CC</sub> = Min, I <sub>OL</sub> = Max V <sub>IH</sub> = Min		0.2	0.4	V
I <sub>I</sub>	Input Current @ Max Input Voltage	V <sub>CC</sub> = Max, V <sub>I</sub> = 5.5V			1	mA
I <sub>IH</sub>	High Level Input Current	V <sub>CC</sub> = Max, V <sub>I</sub> = 2.4V			40	μA
I <sub>IL</sub>	Low Level Input Current	V <sub>CC</sub> = Max, V <sub>I</sub> = 0.4V			-1.6	mA
I <sub>CCH</sub>	Supply Current with Outputs High	V <sub>CC</sub> = Max		4	8	mA
I <sub>CCL</sub>	Supply Current with Outputs Low	V <sub>CC</sub> = Max		12	22	mA

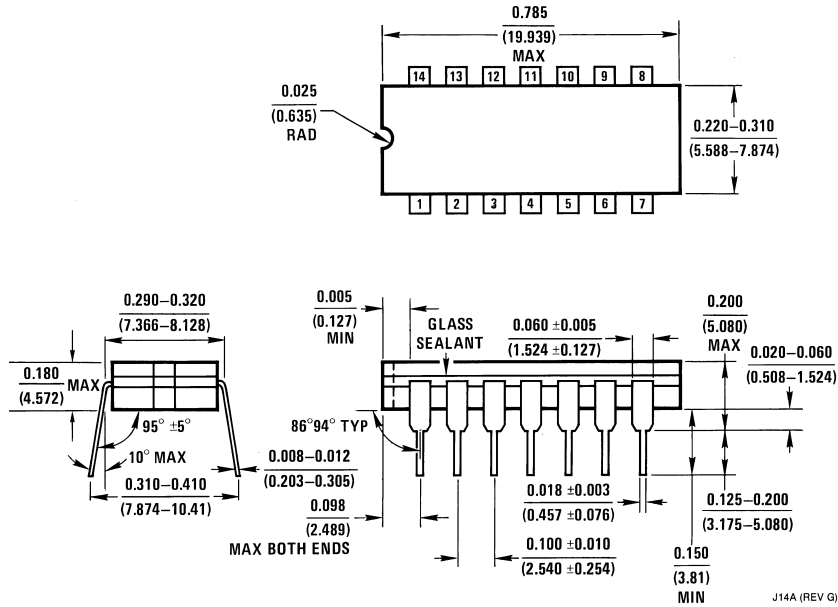
**Switching Characteristics**

at V<sub>CC</sub> = 5V and T<sub>A</sub> = 25°C

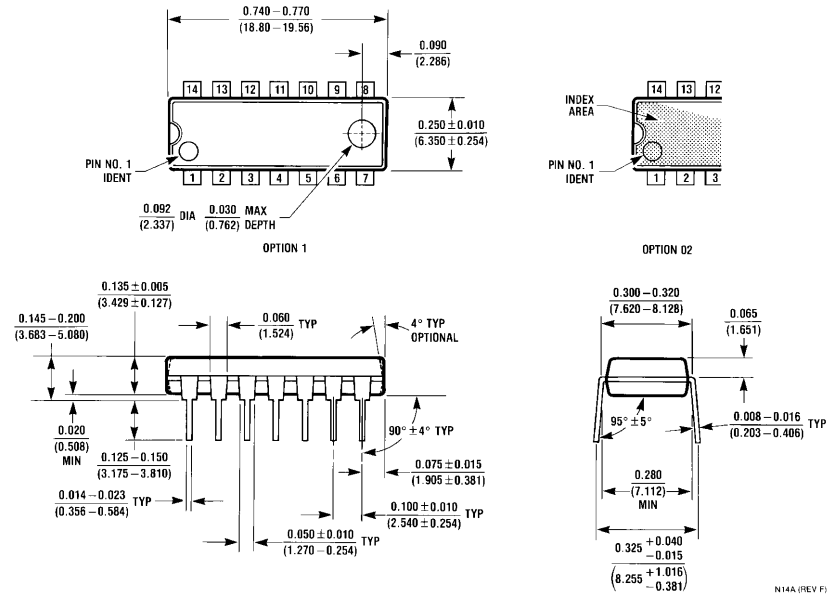
Symbol	Parameter	Conditions	Min	Max	Units
t <sub>PLH</sub>	Propagation Delay Time Low to High Level Output	C <sub>L</sub> = 15 pF R <sub>L</sub> = 4 kΩ (t <sub>PLH</sub> )		45	ns
t <sub>PHL</sub>	Propagation Delay Time High to Low Level Output	R <sub>L</sub> = 400Ω (t <sub>PHL</sub> )		15	ns

**Note 2:** All typicals are at V<sub>CC</sub> = 5V, T<sub>A</sub> = 25°C.

**Physical Dimensions** inches (millimeters) unless otherwise noted



**14-Lead Ceramic Dual-In-Line Package (J)**  
Order Number DM5403J  
Package Number J14A



**14-Lead Molded Dual-In-Line Package (N)**  
Order Number DM7403N  
Package Number N14A

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